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PATENT
0756-2062

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
Shunpei YAMAZAKI) Group: 2811
Serial No.: 09/433,705) Examiner: Unknown
Filed: November 4, 1999)
Title: METHOD OF FABRICATING)
A SEMICONDUCTOR DEVICE)

MAY 04 2000

INFORMATION DISCLOSURE STATEMENT

Honorable Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached PTO-1449 forms be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Applicant is submitting this Information Disclosure Statement before the mailing date of a first Office Action on the merits.

The Examiner's attention is directed to 09/120,290, which is in the family of JP Laid-Open No. 11-345767.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. "1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380 (0756-2062). A duplicate copy of this sheet is attached.

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on 04 May 2000

Respectfully submitted,

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